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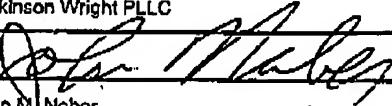
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		Application Number	10/052,466
		Filing Date	January 23, 2002
		First Named Inventor	Ming-Kwei LEE
		Art Unit	2811
		Examiner Name	Owens, Douglas W.
Total Number of Pages in This Submission	4	Attorney Docket Number	70434.00024

ENCLOSURES (Check all that apply)

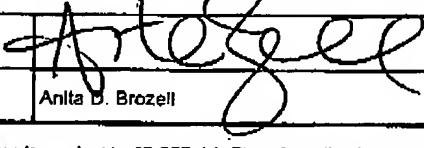
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Dickinson Wright PLLC		
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Date	November 14, 2005	Reg. No.	46,487

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Attorney Docket No.: 70434.00024
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ming-Kwei LEE

Group Art Unit: 2811

Application No.: 10/052,466

Examiner: Owens, Douglas W.

Patent Number: 6,858,873

Confirmation No.: 6427

Issued: February 22, 2005

Filed: January 23, 2002

For: Semiconductor Diode Having A Semiconductor Die With A Substrate and Multiple Films Applied Thereover

REQUEST FOR CERTIFICATE OF
CORRECTION UNDER 37 CFR 1.322

Attention: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Patentee requests that an issued patent be corrected. A copy of the Certificate of Correction, issued by the U.S. Patent and Trademark Office on February 22, 2005, is attached hereto. In support of this, a copy of the first page of the issued patent is attached hereto. The missing assignee data has been circled in red and should read as noted below in the appropriate area of the issued patent.

The name of the Assignee is as follows:

CHIA TA WORLD CO., LTD.
No. 16, Lane 317, Chung-Shan N. Rd.
Yung-Kang City, Tainan Hsien, Taiwan

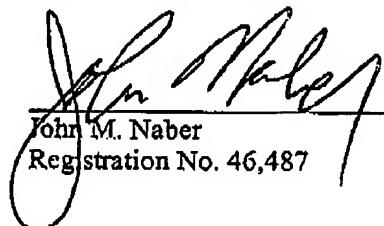
Accordingly, patentee respectfully requests a Certificate of Correction be issued for the referenced patent, and if the Office deems it appropriate, issuance of corrected Letters Patent. Since the error is the result of Patent Office mistake, no fee is required or submitted.

Request for Certificate of Correction
U.S. Patent 6,858,873

Atty Dkt. No.: 70434.00024

However, if this is found to the contrary, please charge any shortage in fees in connection with the filing of this paper, including extension of times fees, to Deposit Account number 04-0160 and please credit any excess fees to such account.

Respectfully submitted,



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(12) United States Patent

Lee

(10) Patent No.: US 6,858,873 B2
(45) Date of Patent: Feb. 22, 2005

(54) SEMICONDUCTOR DIODE HAVING A SEMICONDUCTOR DIE WITH A SUBSTRATE AND MULTIPLE FILMS APPLIED THEREOVER

(76) Inventor: Ming-Kwei Lee, 4F-4, No. 171, Pi-Chung St., Yen-Cheng Dist., Kaohsiung City (TW)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 70 days.

(21) Appl. No.: 10/052,466

(22) Filed: Jan. 23, 2002

(65) Prior Publication Data

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(51) Int. Cl. 7 H01L 29/267

(52) U.S. Cl. 257/81, 257/99

(58) Field of Search 257/81, 79, 84, 257/99, 46

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Primary Examiner—Eddie Lee

Assistant Examiner—Douglas W. Owens

(57) ABSTRACT

A semiconductor diode has a semiconductor die that includes a substrate, a first semiconductor film, a second semiconductor film, a first metal contact, and a second metal contact. The semiconductor die defines two diagonally opposite first corners and two diagonally opposite second corners. The first semiconductor film has an exposed area that is exposed from the second semiconductor film and that extends between one of the first corners and one of the second corners. The first metal contact is formed on the exposed area and has an extension section and a wire-bonding section that has a width greater than that of the extension section and a length less than that of the extension section. The second metal contact extends between the other one of the first corners and the other one of the second corners and has an extension section and a wire-bonding section that has a width greater than that of the extension section and a length less than that of the extension section.

17 Claims, 4 Drawing Sheets

